

Table 2-1. Option 010/020 Commands

Command	Title	Definition
AFn	Analog First Channel	n=0 to 999
ALn	Analog Last Channel	n=0 to 999
AS	Analog Step	increments channel
AEn	Analog External Increment	n=0: OFF, n=1: ON, n=2: Fast Scan
AR	Analog Reset	Opens all analog channels
ACn1,n2,n3, n4	Analog Close	n=0 to 999
AVn	Analog View	n=0 to 999
Aln	Analog increment, close channel, and trigger VM	n=0 to 999

Table 2-2. Specifications for Options 010/020

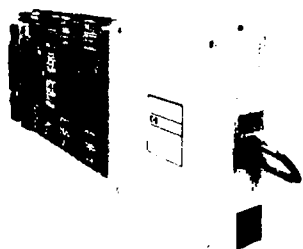
Input Characteristics Maximum Input Voltage: < 170 V peak between any two terminals. Maximum Current: 50 mA per channel non-inductive Maximum Power: 1 VA per channel Thermal Offset: Direct Switched: < 1 μ V Differential Tree Switched: < 2 μ V Differential Closed Channel Resistance: In Series: 100 ohms $\pm$ 10% in High, Low, and Guard Relays only: < 1 ohm			Operating Characteristics: Maximum Switch Rate: 475/second using hardware increment Rated Switch Life at 1 VA: 10^7 operations All relays are Break Before Make		
Isolation (Relays) 25°C, < 85% R.H. 40°C, < 95% R.H.			AC Performance High to Low Capacitance: Channel Open: < 10 pF/channel Channel Closed: < 220 pF/channel Interchannel Capacitance: < 15 pF Cross Talk 100 kHz 1 MHz Channel to Common 50 ohm termination 70 dB 50 dB 1 M ohm termination 50 dB 40 dB Channel to Channel 50 ohm termination 70 dB 32 dB 1 M ohm termination 50 dB 30 dB		
HI To LO Relay Open > 10^{10} Ohm > 10^9 Ohm Relay Closed Option 010 > 10^{10} Ohm > 10^9 Ohm Option 020 > 10^8 Ohm > 10^7 Ohm LO to GUARD > 10^{10} Ohm > 10^7 Ohm GUARD to CHASSIS > 10^{10} Ohm > 10^9 Ohm			Thermocouple Compensation: Reference Junction Compensation Accuracy (23°C $\pm$ 5°C): $\pm$ 0.1°C Temperature Coefficient: (0°C to 18°C, 28°C to 50°C): .009°C/°C Stability: 075°C/1000 hours Temperature across isothermal block < 2°C		



DATA ACQUISITION, CONTROL & TEST

Data Acquisition/Control Unit (cont.)

Model 3497A



Option 030—20 Channel FET Multiplexer Assembly

The option 030 assembly is used to multiplex input signals to a DVM in a manner similar to option 010. The option 030 assembly provides high speed, low level multiplexing. Maximum signal levels are 12 volts peak between any high, low or guard input and any other guard input, guard common or chassis ground.

Maximum sequential scanning rate: 4800 readings/s (at 60 Hz) using an HP 3437A Voltmeter and IIP Series 200 computer; 4000 readings/s at 50 Hz power.

Bias currents: sourced by either high or low to guard

0 28°C		28 55°C
Channel closed:	± 300 nA	Current doubles every 15°C above 28°C
Channel open:	± 15 nA	
From each deselected FET assembly:	± 15 nA	

Differential offset voltage: includes effects of bias currents and series resistance. Does not include effects of voltmeter bias and noise currents.

0-28°C	28 55°C
± 1.4 mV	Add ±140 μ V/°C for each degree above 28°C
±0.15 mV per deselected FET assembly	Add ±15 μ V/°C above 28°C for each selected FET assembly

Series resistance for each input: intrinsic resistance of the FET switch (when ON) plus series protection resistor.

	0-55°C
High, low	5500 Ohms
Guard	3500 Ohms

Maximum current: ± 1 mA per channel

Option 050—16 Channel Isolated Digital Input/Interrupt

The option 050 assembly can sense up to 16 channels of digital data. The first 8 channels can also be used as interrupt lines to detect transient signals. The assembly can accept a wide range of input levels and all functions and masks are fully programmable. A five-volt supply is provided for driving external contact closures and open collector outputs.

Input Signal Characteristics

Input Level	Low Voltage Maximum	High Voltage Minimum	Maximum Input Voltage Between High & Low Terminals	Minimum Input Current
5 V	0.8 V	2.4 V	30 V	400 μ A
12 V	3.0 V	7.0 V	42 V	1 mA
24 V	6.0 V	13.0 V	42 V	2 mA

Maximum voltage: ± 170 V peak between any terminal and chassis

Logic polarity: positive true (negative true is jumper selectable)

Interrupt Mode (bits 0-7)

Minimum pulse width: 100 microseconds

Triggering: each interrupt line is individually programmable for positive or negative edge triggering.

Masking: each interrupt line may be enabled or disabled using a programmable mask.

Option 060—100 kHz Reciprocal Counter

This option can be used to measure mechanical and low frequency electronic signals. The counter can measure the period of signals up to 100 kHz and the pulse width of signals down to 18 μ s. The counter can also count up or down from a programmable start point. It can accept a wide variety of input signals including CMOS, open collector TTL and passive contact closures.

Input Signal Characteristics Input Levels

Input Level Range	V(Lo) (Maximum)		V(Hi) (Minimum)	
	Isolated	Non-iso	Isolated	Non-iso
5 V	1.0 V	1.0 V	4.2 V	4.2 V
12 V	1.8 V	2.7 V	10.3 V	8.0 V
24 V	2.6 V	6.0 V	18.4 V	16.5 V

(5 V level is standard, 12 and 24 volt levels are jumper selectable. Other voltages can be accepted using customer supplied resistors.)

Input circuit: switch selection of optically isolated or non-isolated input. Non-isolated input has 19.5 k Ω minimum input impedance.

Maximum isolation voltage: 170 V peak between any terminal and ground. Isolated mode only.

Period Mode

Maximum input frequency: 100 kHz

Minimum on time: 5 μ s

Minimum off time: 5 μ s

Range Characteristics

Range	Least Significant Digit (LSD)	
	HP-IB	Display
9999.999 s	1 ms	10 ms
99.99999 s	10 μ s	100 μ s
0.9999999 s	100 ns	1 μ s
.09999999 s	10 ns	1 μ s

Accuracy: ± (0.1% of reading + 2 LSDs + Trigger Error)

Pulse Width

Minimum start to stop time: (pulse width): 18 μ s

Minimum stop to start time: 18 μ s

Range Characteristics

Range	Least Significant Digit (LSD)	
	HP-IB	Display
9999.999 s	1 ms	10 ms
99.99999 s	10 μ s	100 μ s
0.999999 s	1 μ s	1 μ s
.099999 s	1 μ s	1 μ s

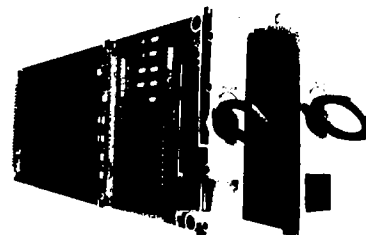
Accuracy: ± (0.1% of reading + Trigger Error + (2 LSDs or 18 μ s, whichever is greater)).

Totalize/Down Count Mode

Maximum input frequency: 100 kHz

Minimum pulse width: 5 μ s

Range: 0 to 999,999



Option 070—120 Ohm Strain Gage/Bridge Completion Assembly

Option 071—350 Ohm Strain Gage/Bridge Completion Assembly



The option 070/071 assemblies may be used to provide bridge completion for measuring strain gages, RTDs pressure sensors and load cells. Each card uses an internal shared half bridge and can complete 10 channels of $\frac{1}{4}$ and $\frac{1}{2}$ and full bridges in any combination. When used with a +5 V excitation supply (such as the HP 6214A) and the HP 3497A DVM, the assembly provides 0.1 μE sensitivity with 1 μE accuracy. Provisions are made for shunt calibration and checking gage leakage and lead resistance.

Specifications

Sensitivity (excitation voltage at 5 volts)			Accuracy	
Bridge Type	HP 3497A DVM	HP 3456A DVM	Range at Best Resolution	90 Day 23°C $\pm$ 5°C
$\frac{1}{4}$	4 μE	04 μE	42400 μE	25 μE
$\frac{1}{2}$	2 μE	02 μE	21200 μE	5 μE
Full	1 μE	01 μE	9500 μE	1 μE

Excitation Supply Requirements

V max: 5.4 Vdc; **I (out):** 250 mA per 10 channels (120 ohm gages)

Option 110—16 Channel Actuator

Option 115—8 Channel High Voltage Actuator

Option 110 consists of 16 mercury wetted form C (single pole-double throw) relays. Each relay can be individually closed and can switch 1A at 100V. The actuator assembly can be used to switch test fixture power or to actuate alarm bells. This flexibility of this assembly allows it to be used as a digital output or matrix switch.

Option 115 is an 8 channel high voltage actuator assembly that can switch voltages up to 252 Vrms and currents up to 2 amperes. The Option 115 assembly is ideal for switching power line voltages to small motors, alarm bells and lights, motor starters and solenoids.

Option 110 and 115 Specifications

	Option 110	Option 115
Switch Form	C	A
Contact Type	Mercury Wetted	Dry
Number of channels	16	8
Maximum Voltage	100 V Peak	252 Vrms 48 Vdc
Maximum Current	1 A	2 Arms or dc
Maximum Power	100 VA	500 VA ac 60 VA dc

Option 120—Dual Voltage D/A

Option 130—Dual Current D/A

Option 120 consists of two 0 to ± 10 V programmable voltage sources. These sources can be used to provide a programmable test stimulus or to control voltage programmed devices like power supplies and VCOs.

Option 130 consists of two 0 to 20 mA or 4 to 20 mA programmable current sources. These sources, especially when using the 4 to 20 mA range, can be used as transmitters in industrial current loops and can drive up to 600 ohms of total loop resistance.

Option 120 Specifications

Output: 13 bits including polarity

Least significant bit: 2.5 mV

Output range: -10.2375 V to +10.2375 V

90 day accuracy: $\pm 0.070\%$ of programmed value ± 4.0 mV

Maximum output current: 15 mA (output within specifications)

Option 130 Specifications

Output: 12 bits

Least significant bit: 5 μA (0 to 20 mA range)

4 μA (4 to 20 mA range)

Output range: 0 to 20.475 mA or 4 to 20.380 mA (each source jumper selectable)

90 day accuracy: $\pm 0.07\%$ of programmed value ± 10.0 μA

Compliance voltage: 12.0 volts

Option 140 Breadboard Card

Option 140 is a breadboard card compatible with the HP 3497A cardage. Using this card, HP 3497A users can construct special purpose assemblies that communicate with the HP 3497A backplane.

Option 232—RS232C Interface

Option 232 to the HP 3497A deletes the standard HP-IB interface and adds an RS232C (CCITT/V.24) compatible interface. The option 232 interface is also compatible with the new RS423 (CCITT/V.10) version of the RS449 interface.

The option 232 interface allows you to remotely locate the 3497A. HP technical brochure part number 5952-8884 contains additional information on HP 3497A option 232.

Option 298—HP 3498A Extender

The HP 3498A Extender chassis allows low cost expansion of HP 3497A-based systems. Each HP 3498A can hold up to ten HP 3497A plug-in assemblies. Use of one or more HP 3498As requires a HP 3497A (for control); all required connecting cables are supplied with the HP 3498A.

Number of slots per HP 3498A: 10

Maximum number of added analog multiplexer channels (options 010, 020): 900 channels (45 assemblies)

Maximum number of added non-analog acquisition assemblies (options 050, 060, 110, 120, 130): 85 assemblies

Maximum number of HP 3498As per HP 3497A: 13

General

HP-IB Interface functions: SH1, AH1, T5, L4, SR1, RL1, PP0, DC1, DT1, C0, E1

Size (HP 3497A or HP 3498A): 190.5 mm H x 428.6 mm W x 520.7 mm D (7 $\frac{1}{2}$ " x 16 $\frac{3}{4}$ " x 20 $\frac{1}{2}$ ".)

Net weight: HP 3497A, 20.4 kg (45 lb) and 3498A, 20.4 kg (45 lb) with assemblies in all slots.

Shipping weight: HP 3497A and HP 3498A maximum with assemblies in all slots are 26.3 kg (58 lb.)

Operating temperature: 0°C to 55°C

Non-operating temperature: -40°C to 75°C

Humidity: to 95% at 40°C except as noted

Operating power: switch selection of 110, 120, 220 and 240 volts $\pm 10\%$, 48-66 Hz, 150 VA 3497A and 3498A.

Ordering Information

Price

Each HP 3497A can hold one DVM assembly (Opt 001) and up to 5 plug-in assemblies. Each HP 3498A (Opt 298) can hold 10 additional plug-ins. To order plug-ins without a mainframe, order as 444XXX Field Installation Kits as shown below.

Required on Every Order:

- A Clock Format (Option 230 or 231)
- A Power Line Frequency and Voltage (Options 315 through 346)

Opt 001 or 44420A: 5 $\frac{1}{2}$ Digit DVM and Current Source \$1,650

Opt 010 or 44421A: 20 Channel Relay Multiplexer Assembly \$600

Opt 020 or 44422A: Relay Multiplexer Assembly with thermo-couple compensation \$750

Also order one of the following no charge options for thermo-couple compensation: Option A20 for software compensation. Option B20, E20, J20, K20, R20, S20 or T20 for hardware compensation of types B,E,J,K,R,S or T. Software compensation works with any of these types.

Opt 030 or 44423A: 20 channel FET Multiplexer Assembly \$750

Opt 050 or 44425A: 16 channel Isolated Digital Input/Interrupt Assembly \$550

Opt 060 or 44426A: 100 kHz Reciprocal Counter \$600

Opt 070 or 44427A: 120 Ohm Strain Gage/Bridge Completion Assembly \$900

Opt 071 or 44427B: 350 Ohm Strain Gage/Bridge Completion Assembly \$900

Opt 110 or 44428A: 16 Channel Actuator/Digital Output Assembly \$750

Opt 115 or 44431A: 8 Channel High Voltage Actuator Assembly \$550

Opt 120 or 44429A: Dual Output Voltage DAC Assembly \$1,000

Opt 130 or 44430A: Dual Output Current DAC Assembly \$1,000

Opt 140 or 44432A: Breadboard Card \$130

Opt 230: Clock Format (Month:Day:Hours:Min:Second) N/C

Opt 231: Clock Format (Day:Month:Hours:Min:Second) N/C

Opt 232: Delete HP-IB Interface, add RS232C Interface \$210

Opt 260: Delete Keyboard and Display less \$360

Opt 298: Add HP 3498A Extender & connecting cables \$2,100

HP 3497A Data Acquisition/Control Unit \$3,000